

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Masamichi FUJITO et al.

Appln. No.:

Filed: HEREWITH

For: SEMICONDUCTOR DEVICE

\* \* \*

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

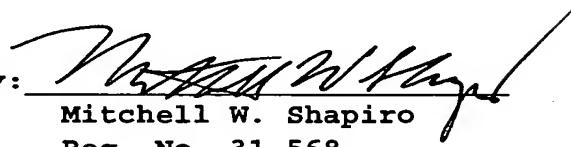
Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the document listed on the attached Form PTO-1449 is hereby cited.

The document on the attached List was cited in the specification, on page 3, and its relevance is indicated therein.

Respectfully submitted,

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March 29, 2004

<b>FORM PTO-1449</b>				Atty. Docket No. <b>XA-10065</b>	Appln. No.		
<b>LIST OF DOCUMENTS CITED BY APPLICANT</b>				Applicant <b>Masamichi FUJITO</b>			
				Filing Date <b>HEREWITH</b>	Group		
<b>U.S. PATENT DOCUMENTS</b>							
Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
<b>FOREIGN PATENT DOCUMENTS</b>							
Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
	AJ	<b>2000-339983</b>	<b>12/8/2000</b>	<b>Japan</b>			<b>Abstract</b>
	AK						
	AL						
	AM						
	AN						
	AO						
<b>OTHER</b> (including author, title, date, pertinent pages, etc.)							
	AP						
	AQ						
	AR						
Examiner				Date Considered			
XAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Enclose copy of this form with next communication to Applicant.							